



ITC

Docket No.: A0312.70515US00
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: John Hayden
Serial No.: 10/786,249
Confirmation No.: 2715
Filed: February 25, 2004
For: DMA CONTROLLER FOR DIGITAL SIGNAL PROCESSORS
Examiner: J. S. Vidwan
Art Unit: 2182

Certificate of Mailing Under 37 CFR 1.8(a)

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the U.S. Postal Service on the date shown below with sufficient postage as First Class Mail, in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Dated: February 27, 2008

June M. Watson

AMENDMENT IN RESPONSE TO NON-FINAL OFFICE ACTION

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action dated November 27, 2007, please amend the above-identified application as follows:

Pending Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 5 of this paper.